



IFW
2114

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/007,625
Filed: November 2, 2001
Inventor(s):
Phelps et al.

Examiner: Lohn, Joshua A.
Group/Art Unit: 2114
Atty. Dkt. No: 5681-43100

Title: SYSTEM FOR DESIGN
VERIFICATION USING
SELECTED STATES OF A
PROCESSOR-BASED
SYSTEM TO REVEAL
DEFICIENCIES

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Stephen J. Curran

Printed Name


Signature

12/21/04
Date

AMENDMENT; RESPONSE TO OFFICE ACTION OF
SEPTEMBER 22, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of September 22, 2004, to further highlight why the application is in condition for allowance.

IN THE TITLE:

Please change the title as shown above to “SYSTEM FOR DESIGN
VERIFICATION USING SELECTED STATES OF A PROCESSOR-BASED SYSTEM
TO REVEAL DEFICIENCIES”